Abstracts

A highly integrated, versatile GPS receiver for E911 applications

L. Wilz, T. Shie, J. Young and T. Hancock. "A highly integrated, versatile GPS receiver for E911 applications." 2002 Radio Frequency Integrated Circuits (RFIC) Symposium 02. (2002 [RFIC]): 115-118.

This paper presents the design of a highly integrated GPS receiver for hand held and other applications. The IC consists of a complete GPS receiver with an integrated multi-mode fractional-N synthesizer. It is one of the most highly integrated GPS receiver ICs presented to date with unmatched flexibility and performance. The receiver chip is fabricated in a 0.35 /spl mu/m silicon BiCMOS process and packaged in a 48 pin 7 mm/spl times/7 mm land grid array chip scale package.

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